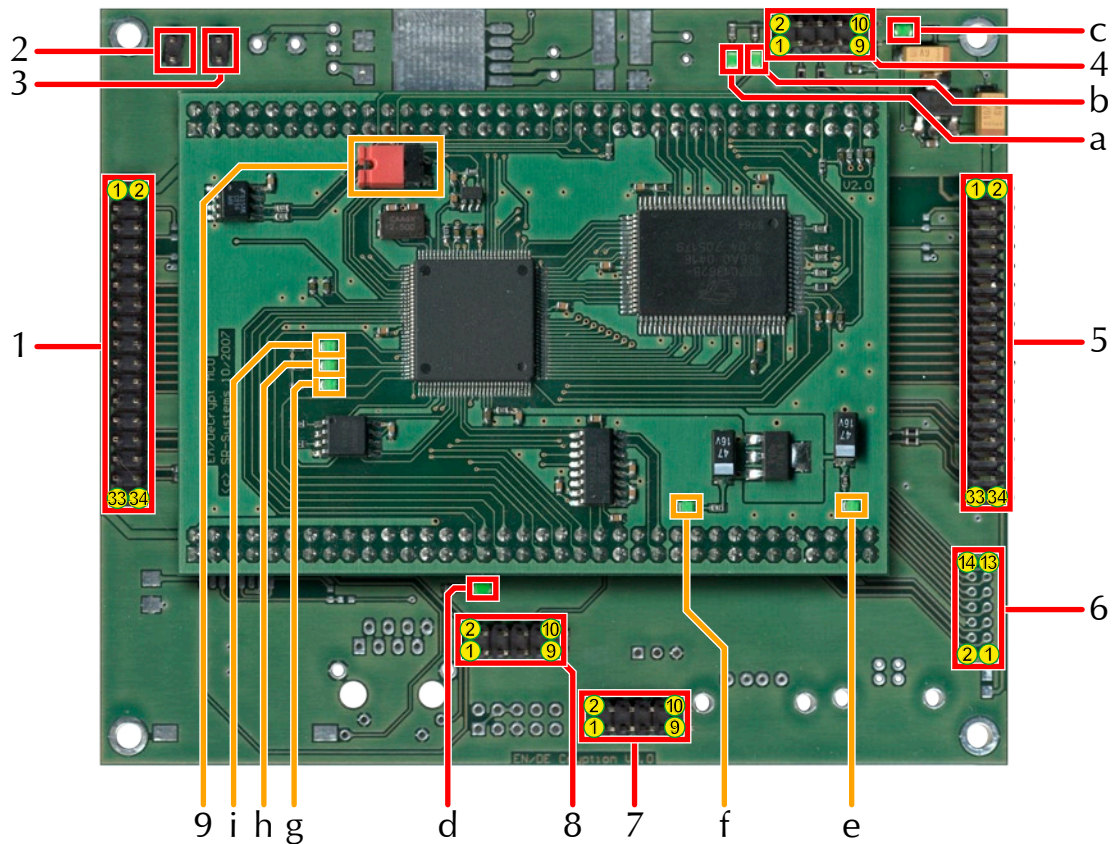


# 1 Connector description



board dimensions: 120×100 mm

|                   |   |              |                              |
|-------------------|---|--------------|------------------------------|
| <b>Connectors</b> | 1 | 34pin Header | TS Input                     |
|                   | 2 | 2pin Jumper  | Reset Jumper (normally open) |
|                   | 3 | 2pin Jumper  | not used                     |
|                   | 4 | 10pin Header | Display and Key-Unit         |
|                   | 5 | 34pin Header | TS Output                    |
|                   | 6 | 14pin Header | SDI                          |
|                   | 7 | 10pin Header | RS232 Control                |
|                   | 8 | 10pin Header | RS232 Data                   |
|                   | 9 | Jumper       | MCU Program if set           |
| <b>LEDs</b>       | a | LED green    | MCU Status 1                 |
|                   | b | LED green    | MCU Status 2                 |
|                   | c | LED green    | FPGA-Board 3,3 V ok          |
|                   | d | LED green    | FPGA loaded and running      |
|                   | e | LED green    | MCU-Board 3,3 V ok           |
|                   | f | LED green    | MCU-Board 5,0 V ok           |
|                   | g | LED green    | MCU Status 2                 |
|                   | h | LED green    | MCU Status 1                 |
|                   | i | LED green    | MCU Status 3                 |

| <b>1 – TS Input</b>  |                 |    |                  |
|----------------------|-----------------|----|------------------|
| 1                    | +5.0 V          | 2  | +5.0 V           |
| 3                    | +5.0 V          | 4  | +5.0 V           |
| 5                    | SDA             | 6  | not connected    |
| 7                    | SCL             | 8  | xReset           |
| 9                    | GND             | 10 | GND              |
| 11                   | TSCLK in/out    | 12 | PSYM             |
| 13                   | not connected   | 14 | DVAL             |
| 15                   | TS 6            | 16 | TS 7             |
| 17                   | TS 4            | 18 | TS 5             |
| 19                   | TS 2            | 20 | TS 3             |
| 21                   | TS 0            | 22 | TS 1             |
| 23                   | GND             | 24 | GND              |
| 25                   | SD Out (f. E.*) | 26 | PLL THR (f. E.*) |
| 27                   | SDCLK (f. E.*)  | 28 | SD In (f. E.*)   |
| 29                   | GND             | 30 | GND              |
| 31                   | MCLK 27 MHz     | 32 | ASCLK (f. E.*)   |
| 33                   | RST Vid. Codec  | 34 | not connected    |
| *f.E. = for Encoders |                 |    |                  |

| <b>4 – Display- and Key-Unit</b> |        |    |     |
|----------------------------------|--------|----|-----|
| 1                                | +5,0 V | 2  | 5V  |
| 3                                | SDA    | 4  | SDA |
| 5                                | SCL    | 6  | SCL |
| 7                                | Reset  | 8  | IRQ |
| 9                                | GND    | 10 | GND |

| <b>5 – TS Output</b> |                 |    |                  |
|----------------------|-----------------|----|------------------|
| 1                    | +5.0 V          | 2  | +5.0 V           |
| 3                    | +5.0 V          | 4  | +5.0 V           |
| 5                    | SDA             | 6  | not connected    |
| 7                    | SCL             | 8  | xReset           |
| 9                    | GND             | 10 | GND              |
| 11                   | TSCLK in/out    | 12 | PSYM             |
| 13                   | not connected   | 14 | DVAL             |
| 15                   | TS 6            | 16 | TS 7             |
| 17                   | TS 4            | 18 | TS 5             |
| 19                   | TS 2            | 20 | TS 3             |
| 21                   | TS 0            | 22 | TS 1             |
| 23                   | GND             | 24 | GND              |
| 25                   | SD Out (f. E.*) | 26 | PLL THR (f. E.*) |
| 27                   | SDCLK (f. E.*)  | 28 | SD In (f. E.*)   |
| 29                   | GND             | 30 | GND              |
| 31                   | MCLK 27 MHz     | 32 | ASCLK (f. E.*)   |
| 33                   | RST Vid. Codec  | 34 | not connected    |
| *f.E. = for Encoders |                 |    |                  |

| <b>6 – SDI (ITU-656)</b> |               |    |               |
|--------------------------|---------------|----|---------------|
| 1                        | +3.3 V        | 2  | GND           |
| 3                        | not connected | 4  | not connected |
| 5                        | VCLK          | 6  | GND           |
| 7                        | DVideo 0      | 8  | DVideo 1      |
| 9                        | DVideo 2      | 10 | DVideo 3      |
| 11                       | DVideo 4      | 12 | DVideo 5      |
| 13                       | DVideo 6      | 14 | DVideo 7      |

| <b>7 – RS232 Control</b> |                 |    |                 |
|--------------------------|-----------------|----|-----------------|
| 1                        | con. with 4+6   | 2  | TxD             |
| 3                        | RxD             | 4  | con with 1+6    |
| 5                        | GND             | 6  | con with 1+4    |
| 7                        | con. with Pin 8 | 8  | con. with Pin 7 |
| 9                        | not connected   | 10 | GND             |

| <b>8 – RS232 Data</b> |               |    |              |
|-----------------------|---------------|----|--------------|
| 1                     | con. with 4+6 | 2  | RxD          |
| 3                     | TxD           | 4  | con with 1+6 |
| 5                     | GND           | 6  | con with 1+4 |
| 7                     | RTS           | 8  | CTS          |
| 9                     | not connected | 10 | GND          |

## Errata/corrections:

07.12.2009 Initial release.

The information in this manual was compiled with high care and to our best knowledge; nevertheless there might be some errors left in this document. We do not take legal or any other responsibility for the correctness of any information.

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We are happy to receive your comments and questions:

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